FEATURES

- Complete Buck-Boost Switch Mode Power Supply
- Wide Input Voltage Range: 5V to 58V
- 12V/1.7A Output from 6V\textsubscript{IN}
- 12V/3.4A Output from 12V\textsubscript{IN}
- 12V/5.4A Output from 24V\textsubscript{IN}
- Up to 96% Efficient
- Adjustable Input and Output Average Current Limits
- Input and Output Current Monitors
- Parallelable for Increased Output Current
- Wide Output Voltage Range: 1.2V to 48V
- Selectable Switching Frequency: 100kHz to 800kHz
- Synchronization from 200kHz to 700kHz
- 15mm × 15mm × 4.92mm BGA Package

APPLICATIONS

- High Power Battery-Operated Devices
- Industrial Control
- Solar Powered Voltage Regulator
- Solar Powered Battery Charging

DESCRIPTION

The LTM\textsuperscript{®}8056 is a 58V\textsubscript{IN}, buck-boost \textsuperscript{µ}Module\textsuperscript{®} (micromodule) regulator. Included in the package are the switching controller, power switches, inductor and support components. A resistor to set the switching frequency, a resistor divider to set the output voltage, and input and output capacitors are all that are needed to complete the design. Other features such as input and output average current regulation may be implemented with just a few components. The LTM8056 operates over an input voltage range of 5V to 58V, and can regulate output voltages between 1.2V and 48V. The SYNC input and CLKOUT output allow easy synchronization.

The LTM8056 is housed in a compact overmolded ball grid array (BGA) package suitable for automated assembly by standard surface mount equipment. The LTM8056 is available with SnPB or RoHS compliant terminal finish.

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TYPICAL APPLICATION

24V\textsubscript{OUT} from 7V\textsubscript{IN} to 58V\textsubscript{IN} Buck-Boost Regulator

Max Output Current and Efficiency vs \textsubscript{VIN}
**LTM8056**

**ABSOLUTE MAXIMUM RATINGS**

(Nota 1)

- \(V_{IN}, SV_{IN}, V_{OUT}, I_{IN}, I_{OUT}\) Voltage ................. 60V
- \(FB, SYNC, CTL, MODE\) Voltage ............................... 6V
- \(I_{INMON}, I_{OUTMON}\) Voltage ................................ 6V
- \(LL\) Voltage ......................................................... 15V

Maximum Junction Temperature (Notes 2, 3) ...... 125°C

Storage Temperature ............................................. 125°C

Peak Solder Reflow Body Temperature ............ 245°C

**ORDER INFORMATION**

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>BALL FINISH</th>
<th>PART MARKING*</th>
<th>DEVICE</th>
<th>FINISH CODE</th>
<th>PACKAGE TYPE</th>
<th>MSL RATING</th>
<th>TEMPERATURE RANGE (Note 2)</th>
</tr>
</thead>
<tbody>
<tr>
<td>LTM8056EY#PBF</td>
<td>SAC305 (RoHS)</td>
<td>LTM8056Y</td>
<td>e1</td>
<td>BGA</td>
<td>3</td>
<td>-40°C to 125°C</td>
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<tr>
<td>LTM8056IY#PBF</td>
<td>SAC305 (RoHS)</td>
<td>LTM8056Y</td>
<td>e1</td>
<td>BGA</td>
<td>3</td>
<td>-40°C to 125°C</td>
<td></td>
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<tr>
<td>LTM8056IY</td>
<td>SnPb (63/37)</td>
<td>LTM8056Y</td>
<td>e0</td>
<td>BGA</td>
<td>3</td>
<td>-40°C to 125°C</td>
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<tr>
<td>LTM8056MPY#PBF</td>
<td>SAC305 (RoHS)</td>
<td>LTM8056Y</td>
<td>e1</td>
<td>BGA</td>
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<td>-55°C to 125°C</td>
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<tr>
<td>LTM8056MPY</td>
<td>SnPb (63/37)</td>
<td>LTM8056Y</td>
<td>e0</td>
<td>BGA</td>
<td>3</td>
<td>-55°C to 125°C</td>
<td></td>
</tr>
</tbody>
</table>

Consult Marketing for parts specified with wider operating temperature ranges.

*Device temperature grade is indicated by a label on the shipping container. Pad or ball finish code is per IPC/JEDEC J-STD-609.

• Terminal Finish Part Marking:
  [www.linear.com/leadfree](http://www.linear.com/leadfree)

• Recommended LGA and BGA PCB Assembly and Manufacturing Procedures:
  [www.linear.com/umodule/pcbassembly](http://www.linear.com/umodule/pcbassembly)

• LGA and BGA Package and Tray Drawings:
  [www.linear.com/packaging](http://www.linear.com/packaging)
## ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ C$. RUN = 1.5V unless otherwise noted. (Note 2)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Minimum Input Voltage</td>
<td>$V_{IN} = SV_{IN}$</td>
<td>●</td>
<td>5.0</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Output DC Voltage</td>
<td>$FB = V_{OUT}$ Through 100k</td>
<td>1.2</td>
<td>48</td>
<td>V</td>
<td></td>
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<tr>
<td></td>
<td>$I_{OUT} = 0.1A$, $R_{FB} = 100k/2.55k$</td>
<td>1.7</td>
<td>4</td>
<td>A</td>
<td></td>
</tr>
<tr>
<td>Output DC Current</td>
<td>$V_{IN} = 6V$, $V_{OUT} = 12V$</td>
<td>8</td>
<td>30</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td></td>
<td>$V_{IN} = 48V$, $V_{OUT} = 12V$</td>
<td>45</td>
<td>100</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>Quiescent Current Into $V_{IN}$ (Tied to $SV_{IN}$)</td>
<td>RUN = 0.3V (Disabled)</td>
<td>0.1</td>
<td>1</td>
<td>µA</td>
<td></td>
</tr>
<tr>
<td></td>
<td>No Load, MODE = 0.3V (DCM)</td>
<td>8</td>
<td>30</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td></td>
<td>No Load, MODE = 1.5V (FCM)</td>
<td>45</td>
<td>100</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>Output Voltage Line Regulation</td>
<td>$5V &lt; V_{IN} &lt; 58V$, $I_{OUT} = 1A$</td>
<td>0.5</td>
<td>%</td>
<td></td>
<td></td>
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<tr>
<td>Output Voltage Load Regulation</td>
<td>$V_{IN} = 12V$, $0.1A &lt; I_{OUT} &lt; 3.5A$</td>
<td>0.5</td>
<td>%</td>
<td></td>
<td></td>
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<tr>
<td>Output RMS Voltage Ripple</td>
<td>$V_{IN} = 24V$, $I_{OUT} = 3A$</td>
<td>25</td>
<td>mV</td>
<td></td>
<td></td>
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<tr>
<td>Switching Frequency</td>
<td>$RT = 453k$</td>
<td>100</td>
<td>kHz</td>
<td></td>
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<tr>
<td></td>
<td>$RT = 45.9k$</td>
<td>800</td>
<td>kHz</td>
<td></td>
<td></td>
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<tr>
<td>Voltage at FB Pin</td>
<td>●</td>
<td>1.188</td>
<td>1.212</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td></td>
<td>●</td>
<td>1.176</td>
<td>1.220</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>RUN Falling Threshold</td>
<td>LTM8056 Stops Switching</td>
<td>●</td>
<td>1.15</td>
<td>1.25</td>
<td>V</td>
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<tr>
<td>RUN Hysteresis</td>
<td>LTM8056 Starts Switching</td>
<td>25</td>
<td>mV</td>
<td></td>
<td></td>
</tr>
<tr>
<td>RUN Low Threshold</td>
<td>LTM8056 Disabled</td>
<td>0.3</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>RUN Pin Current</td>
<td>RUN = 1V</td>
<td>2</td>
<td>3</td>
<td>5</td>
<td>µA</td>
</tr>
<tr>
<td></td>
<td>RUN = 1.6V</td>
<td>50</td>
<td>100</td>
<td>nA</td>
<td></td>
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<tr>
<td>$I_{IN}$ Bias Current</td>
<td></td>
<td>90</td>
<td>µA</td>
<td></td>
<td></td>
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<tr>
<td>Input Current Sense Threshold ($I_{IN}$-$V_{IN}$)</td>
<td>●</td>
<td>44</td>
<td>56</td>
<td>mV</td>
<td></td>
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<tr>
<td>$I_{OUT}$ Bias Current</td>
<td></td>
<td>20</td>
<td>µA</td>
<td></td>
<td></td>
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<tr>
<td>Output Current Sense Threshold ($V_{OUT}$-$I_{OUT}$)</td>
<td>$V_{CTL} = \text{Open}$</td>
<td>54.5</td>
<td>61.5</td>
<td>mV</td>
<td></td>
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<tr>
<td></td>
<td></td>
<td>53</td>
<td>63</td>
<td>mV</td>
<td></td>
</tr>
<tr>
<td>$I_{IN\text{MON}}$ Voltage</td>
<td>LTM8056 in Input Current Limit</td>
<td>0.96</td>
<td>1.04</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>$I_{OUT\text{MON}}$ Voltage</td>
<td>LTM8056 in Output Current Limit</td>
<td>1.14</td>
<td>1.26</td>
<td>V</td>
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<tr>
<td>CTL Input Bias Current</td>
<td>$V_{CTL} = 0V$</td>
<td>22</td>
<td>µA</td>
<td></td>
<td></td>
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<tr>
<td>SS Pin Current</td>
<td>$V_{SS} = 0V$</td>
<td>35</td>
<td>µA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>CLKOUT Output High</td>
<td>10k to GND</td>
<td>4</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>CLKOUT Output Low</td>
<td>10k to 5V</td>
<td>0.7</td>
<td>V</td>
<td></td>
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<tr>
<td>SYNC Input Low Threshold</td>
<td></td>
<td>0.3</td>
<td>V</td>
<td></td>
<td></td>
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<tr>
<td>SYNC Input High Threshold</td>
<td></td>
<td>1.5</td>
<td>V</td>
<td></td>
<td></td>
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<tr>
<td>SYNC Bias Current</td>
<td>SYNC = 1V</td>
<td>11</td>
<td>µA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>MODE Input Low Threshold</td>
<td></td>
<td>0.3</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>MODE Input High Threshold</td>
<td></td>
<td>1.5</td>
<td>V</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Note 1:** Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

**Note 2:** The LTM8056E is guaranteed to meet performance specifications from 0°C to 125°C internal. Specifications over the full –40°C to 125°C internal operating temperature range are assured by design, characterization and correlation with statistical process controls. The LTM8056I is guaranteed to meet specifications over the full –40°C to 125°C internal operating temperature range. The LTM8056MP is guaranteed to meet specifications over the full –55°C to 125°C internal operating temperature range. Note that the maximum internal temperature is determined by specific operating conditions in conjunction with board layout, the rated package thermal resistance and other environmental factors.

**Note 3:** The LTM8056 contains overtemperature protection that is intended to protect the device during momentary overload conditions. The internal temperature exceeds the maximum operating junction temperature when the overtemperature protection is active. Continuous operation above the specified maximum operating junction temperature may impair device reliability.
TYPICAL PERFORMANCE CHARACTERISTICS  $T_A = 25^\circ C$, unless otherwise noted.

**Efficiency vs Output Current**
- (3.3V$_{OUT}$)
- (5V$_{OUT}$)
- (8V$_{OUT}$)
- (12V$_{OUT}$)
- (18V$_{OUT}$)
- (24V$_{OUT}$)
- (36V$_{OUT}$)
- (48V$_{OUT}$)

*Input Current vs Output Current (3.3V$_{OUT}$)*
TYPICAL PERFORMANCE CHARACTERISTICS  \( T_A = 25^\circ C \), unless otherwise noted.
TYPICAL PERFORMANCE CHARACTERISTICS  \( T_A = 25^\circ C, \) unless otherwise noted.

**Maximum Output Current vs \( V_{IN} \)**

**Temperature Rise vs Output Current (3.3V\(_{OUT}\))**

**Temperature Rise vs Output Current (5V\(_{OUT}\))**

**Temperature Rise vs Output Current (8V\(_{OUT}\))**

**Temperature Rise vs Output Current (12V\(_{OUT}\))**

**Temperature Rise vs Output Current (18V\(_{OUT}\))**

**Temperature Rise vs Output Current (24V\(_{OUT}\))**

**Temperature Rise vs Output Current (36V\(_{OUT}\))**

**Temperature Rise vs Output Current (48V\(_{OUT}\))**
**PIN FUNCTIONS**

**GND (Bank 1, Pin L1):** Tie these GND pins to a local ground plane below the LTM8056 and the circuit components. In most applications, the bulk of the heat flow out of the LTM8056 is through these pads, so the printed circuit design has a large impact on the thermal performance of the part. See the PCB Layout and Thermal Considerations sections for more details. Return the RFB1/RFB2 feedback divider to this net.

**VOUT (Bank 2):** Power Output Pins. Apply output filter capacitors between these pins and GND pins.

**VIN (Bank 3):** Input Power. The VIN pin supplies current to the LTM8056’s internal power switches and to one terminal of the optional input current sense resistor. This pin must be locally bypassed with an external, low ESR capacitor; see Table 1 for recommended values.

**IOUT (Pin D1):** Output Current Sense. Tie this pin to the output current sense resistor. The output average current sense threshold is 58mV, so the LTM8056 will regulate the output current to 58mV/RSENSE, where RSENSE is the value of the output current sense resistor in ohms. The load is powered through the sense resistor connected at this pin. Tie this pin to VOUT if no output current sense resistor is used. Keep this pin within ±0.5V of VOUT under all conditions.

**LL (Pin F1):** Light Load Indicator. This pin indicates that the output current, as sensed through the resistor connected between VOUT and IOUT, is approximately equivalent to 10mV or less. Its state is meaningful only if a current sense resistor is applied between VOUT and IOUT. This is useful to change the switching behavior of the LTM8056 in light load conditions.

**SVIN (Pins F10, F11):** Controller Power Input. Apply a separate voltage above 5V if the LTM8056 is required to operate when the main power input (VIN) is below 5V. Bypass these pins with a high quality, low ESR capacitor. If a separate supply is not used, connect these pins to VIN.

**CLKOUT (Pin G1):** Clock Output. Use this pin as a clock source when synchronizing other devices to the switching frequency of the LTM8056. When this function is not used, leave this pin open.

**MODE (Pin G2):** Switching Mode Input. The LTM8056 operates in forced continuous mode when MODE is open, and can operate in discontinuous switching mode when MODE is low. In discontinuous switching mode, the LTM8056 will block reverse inductor current. This pin is normally left open or tied to LL. This pin may be tied to GND for the purpose of blocking reverse current if no output sense resistor is used.
PIN FUNCTIONS

RT (Pin H1): Timing Resistor. The RT pin is used to program the switching frequency of the LTM8056 by connecting a resistor from this pin to ground. The range of oscillation is 100kHz to 800kHz. The Applications Information section of the data sheet includes a table to determine the resistance value based on the desired switching frequency. Minimize capacitance at this pin. A resistor to ground must be applied under all circumstances.

SYNC (Pin H2): External Synchronization Input. The SYNC pin has an internal pull-down resistor. See the Synchronization section in Applications Information for details. Tie this pin to GND when not used.

FB (Pin J1): Output Voltage Feedback. The LTM8056 regulates the FB pin to 1.2V. Connect the FB pin to a resistive divider between the output and GND to set the output voltage. See Table 1 for recommended FB divider resistor values.

COMP (Pin J2): Compensation Pin. The LTM8056 is equipped with internal compensation that works well with most applications. In some cases, the performance of the LTM8056 can be enhanced by modifying the control loop compensation by applying a capacitor or RC network to this pin.

SS (Pin K1): Soft-Start. Connect a capacitor from this pin to GND to increase the soft-start time. Soft-start reduces the input power source’s surge current by gradually increasing the controller’s current limit. Larger values of the soft-start capacitor result in longer soft-start times. If no soft-start is required, leave this pin open.

CTL (Pin K2): Current Sense Adjustment. Apply a voltage below 1.2V to reduce the current limit threshold of IOUT. Drive CTL to less than about 50mV to stop switching. The CTL pin has an internal pull-up resistor to 2V. If not used, leave this pin open.

IOUTMON (Pin L2): Output Current Monitor. This pin produces a voltage that is proportional to the voltage between VOUT and IOUT. IOUTMON will equal 1.2V when VOUT – IOUT = 58mV. This feature is generally useful only if a current sense resistor is applied between VOUT and IOUT.

IINMON (Pin L3): Input Current Monitor. This pin produces a voltage that is proportional to the voltage between IIN and VIN. IINMON will equal 1V when IIN – VIN = 50mV. This feature is generally useful only if a current sense resistor is applied between VIN and IIN.

RUN (Pin L4): LTM8056 Enable. Raise the RUN pin voltage above 1.2V for normal operation. Above 1.2V (typical), but below 6V, the RUN pin input bias current is less than 1μA. Below 1.2V and above 0.3V, the RUN pin sinks 3μA so the user can define the hysteresis with the external resistor selection. This will also reset the soft-start function. If RUN is 0.3V or less, the LTM8056 is disabled and the SVIN quiescent current is below 1μA.

IIN (Pin L9): Input Current Sense. Tie this pin to the input current sense resistor. The input average current sense threshold is 50mV, so the LTM8056 will regulate the input current to 50mV/RSENSE, where RSENSE is the value of the input current sense resistor in ohms. Tie to VIN when not used. Keep this pin within ±0.5V of VIN under all conditions.
The LTM8056 is a standalone nonisolated buck-boost switching DC/DC power supply. The buck-boost topology allows the LTM8056 to regulate its output voltage for input voltages both above and below the magnitude of the output, and the maximum output current depends upon the input voltage. Higher input voltages yield higher maximum output current.

This converter provides a precisely regulated output voltage programmable via an external resistor divider from 1.2V to 48V. The input voltage range is 5V to 58V, but the LTM8056 may be operated at lower input voltages if SVIN is powered by a voltage source above 5V. A simplified block diagram is given on the previous page.

The LTM8056 contains a current mode controller, power switching elements, power inductor and a modest amount of input and output capacitance. The LTM8056 is a fixed frequency PWM regulator. The switching frequency is set by connecting the appropriate resistor value from the RT pin to GND.

The output voltage of the LTM8056 is set by connecting the FB pin to a resistor divider between the output and GND.

In addition to regulating its output voltage, the LTM8056 is equipped with average current control loops for both the input and output. Add a current sense resistor between IIN and VIN to limit the input current below some maximum value. The IINMON pin reflects the current flowing through the sense resistor between IIN and VIN.

A current sense resistor between VOUT and IOUT allows the LTM8056 to accurately regulate its output current to a maximum value set by the value of the sense resistor.

In general, the LTM8056 should be used with an output sense resistor to limit the maximum output current, as buck-boost regulators are capable of delivering large currents when the output voltage is lower than the input, if demanded.

Furthermore, while the LTM8056 does not require an output sense resistor to operate, it uses information from the sense resistor to optimize its performance. If an output sense resistor is not used, the efficiency or output ripple may degrade, especially if the current through the integrated inductor is discontinuous. In some cases, an output sense resistor is required to adequately protect the LTM8056 against output overload or short-circuit.

A voltage less than 1.2V applied to the CTL pin reduces the maximum output current if an output current sense resistor is used. Drive CTL to less than about 50mV to stop switching. The current flowing through the sense resistor is reflected by the output voltage of the IOUTMON pin.

Driving the SYNC pin will synchronize the LTM8056 to an external clock source. The CLKOUT pin sources a signal that is the same frequency but approximately 180° out of phase with the internal oscillator.

If more output current is required than a single LTM8056 can provide, multiple devices may be operated in parallel. Refer to the Parallel Operation section of Applications Information for more details.

An internal regulator provides power to the control circuitry and the gate driver to the power MOSFETs. This internal regulator draws power from the SVIN pin. The RUN pin is used to place the LTM8056 in shutdown, disconnecting the output and reducing the input current to less than 1μA.

The LTM8056 is equipped with a thermal shutdown that inhibits power switching at high junction temperatures. The activation threshold of this function is above 125°C to avoid interfering with normal operation, so prolonged or repetitive operation under a condition in which the thermal shutdown activates may damage or impair the reliability of the device.
APPLICATIONS INFORMATION

For most applications, the design process is straightforward, summarized as follows:

1. Look at Table 1 and find the row that has the desired input range and output voltage.
2. Apply the recommended \( C_{IN}, C_{OUT}, R_{FB1}/R_{FB2} \) and \( R_T \) values.
3. Apply the output sense resistor to set the output current limit. The output current is limited to \( 58\text{mV}/R_{\text{SENSE}} \), where \( R_{\text{SENSE}} \) is the value of the output current sense resistor in ohms.

While these component combinations have been tested for proper operation, it is incumbent upon the user to verify proper operation over the intended system’s line, load and environmental conditions. Bear in mind that the maximum output current is limited by junction temperature, the relationship between the input and output voltage magnitude and other factors. Please refer to the graphs in the Typical Performance Characteristics section for guidance.

The maximum frequency (and attendant \( R_T \) value) at which the LTM8056 should be allowed to switch is given in Table 1 in the \( f_{\text{MAX}} \) column, while the recommended frequency (and \( R_T \) value) for optimal efficiency over the given input condition is given in the \( f_{\text{OPTIMAL}} \) column. There are additional conditions that must be satisfied if the synchronization function is used. Please refer to the Synchronization section for details.

Note that Table 1 calls out both ceramic and electrolytic output capacitors. Both of the capacitors called out in the table must be applied to the output. The electrolytic capacitors in Table 1 are described by voltage rating, value and ESR. The voltage rating of the capacitor may be increased if the application requires a higher voltage stress derating. The LTM8056 can tolerate variation in the ESR; other capacitors with different ESR may be used, but the user must verify proper operation over line, load and environmental conditions. Table 2 gives the description and part numbers of electrolytic capacitors used in the LTM8056 development testing and design validation.

### Table 1. Recommended Component Values and Configuration (\( T_A = 25^\circ\text{C} \))

<table>
<thead>
<tr>
<th>( V_{IN \ \text{RANGE}} )</th>
<th>( V_{OUT} )</th>
<th>( C_{IN} )</th>
<th>( C_{OUT} )</th>
<th>( R_{FB1}/R_{FB2} )</th>
<th>( f_{\text{OPTIMAL}} ) (kHz)</th>
<th>( R_T(\text{OPTIMAL}) )</th>
<th>( f_{\text{MAX}} ) (kHz)</th>
<th>( R_T(\text{MAX}) )</th>
</tr>
</thead>
<tbody>
<tr>
<td>5V to 24V</td>
<td>3.3V</td>
<td>2 × 4.7µF, 50V, 0805</td>
<td>22µF, 6.3V, X5R, 0805 100µF, 6V, 75mΩ, Electrolytic</td>
<td>100k/56.2k</td>
<td>650</td>
<td>31.6k</td>
<td>800</td>
<td>24.9k</td>
</tr>
<tr>
<td>5V to 22V</td>
<td>5V</td>
<td>2 × 4.7µF, 50V, 0805</td>
<td>22µF, 6.3V, X5R, 0805 100µF, 6V, 75mΩ, Electrolytic</td>
<td>100k/31.6k</td>
<td>450</td>
<td>53.6k</td>
<td>800</td>
<td>24.9k</td>
</tr>
<tr>
<td>5V to 28V</td>
<td>8V</td>
<td>2 × 4.7µF, 50V, 0805</td>
<td>22µF, 10V, X7R, 1206 100µF, 16V, 100mΩ, Electrolytic</td>
<td>100k/17.4k</td>
<td>500</td>
<td>45.3k</td>
<td>800</td>
<td>24.9k</td>
</tr>
<tr>
<td>5V to 41V</td>
<td>12V</td>
<td>2 × 4.7µF, 50V, 0805</td>
<td>22µF, 25V, X5R, 0805 68µF, 25V, 200mΩ, Electrolytic</td>
<td>100k/11k</td>
<td>650</td>
<td>31.6k</td>
<td>800</td>
<td>24.9k</td>
</tr>
<tr>
<td>5.8V to 58V</td>
<td>18V</td>
<td>3 × 2.2µF, 100V, 1206</td>
<td>22µF, 25V, X5R, 0805 47µF, 25V, 900mΩ, Electrolytic</td>
<td>100k/6.98k</td>
<td>650</td>
<td>31.6k</td>
<td>800</td>
<td>24.9k</td>
</tr>
<tr>
<td>7V to 58V</td>
<td>24V</td>
<td>3 × 2.2µF, 100V, 1206</td>
<td>22µF, 25V, X5R, 0805 33µF, 35V 300mΩ, Electrolytic</td>
<td>100k/5.23k</td>
<td>525</td>
<td>43.2k</td>
<td>800</td>
<td>24.9k</td>
</tr>
<tr>
<td>8.5V to 58V</td>
<td>36V</td>
<td>3 × 2.2µF, 100V, 1206</td>
<td>10µF, 50V, X5R, 1206 10µF, 50V 120mΩ, Electrolytic</td>
<td>100k/3.40k</td>
<td>500</td>
<td>45.3k</td>
<td>800</td>
<td>24.9k</td>
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<tr>
<td>12.5V to 58V</td>
<td>48V</td>
<td>3 × 2.2µF, 100V, 1206</td>
<td>10µF, 50V, X5R, 1206 10µF, 63V 120mΩ, Electrolytic</td>
<td>100k/2.55k</td>
<td>475</td>
<td>49.9k</td>
<td>800</td>
<td>24.9k</td>
</tr>
</tbody>
</table>

**Notes:** An input bulk capacitor is required. The output capacitance uses a combination of a ceramic and electrolytic in parallel. Other combinations of resistor values for the RFB network are acceptable.
Capacitor Selection Considerations

The $C_{IN}$ and $C_{OUT}$ capacitor values in Table 1 are the minimum recommended values for the associated operating conditions. Applying capacitor values below those indicated in Table 1 is not recommended, and may result in undesirable operation. Using larger values is generally acceptable, and can yield improved dynamic response, if it is necessary. Again, it is incumbent upon the user to verify proper operation over the intended system’s line, load and environmental conditions.

Ceramic capacitors are small, robust and have very low ESR. However, not all ceramic capacitors are suitable. X5R and X7R types are stable over temperature and applied voltage and give dependable service. Other types, including Y5V and Z5U have very large temperature and voltage coefficients of capacitance. In an application circuit they may have only a small fraction of their nominal capacitance resulting in much higher output voltage ripple than expected.

A final precaution regarding ceramic capacitors concerns the maximum input voltage rating of the LTM8056. A ceramic input capacitor combined with trace or cable inductance forms a high Q (underdamped) tank circuit. If the LTM8056 circuit is plugged into a live supply, the input voltage can ring to twice its nominal value, possibly exceeding the device’s rating. This situation is easily avoided; see the Hot-Plugging Safely section.

Frequency Selection

The LTM8056 uses a constant frequency PWM architecture that can be programmed to switch from 100kHz to 800kHz by tying a resistor from the RT pin to ground. Table 3 provides a list of $R_T$ resistor values and their resultant frequencies.

<table>
<thead>
<tr>
<th>FREQUENCY</th>
<th>$R_T$ VALUE (kΩ)</th>
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<tbody>
<tr>
<td>100</td>
<td>453</td>
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<td>200</td>
<td>147</td>
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<td>300</td>
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<td>700</td>
<td>29.4</td>
</tr>
<tr>
<td>800</td>
<td>24.9</td>
</tr>
</tbody>
</table>

An external resistor within the range stated in Table 3 from RT to GND is required. Even when synchronizing to an external clock. When synchronizing the switching of the LTM8056 to an external signal source, the frequency range is 200kHz to 700kHz.

Operating Frequency Trade-Offs

It is recommended that the user apply the optimal $R_T$ value given in Table 1 for the input and output operating condition. System level or other considerations, however, may necessitate another operating frequency. While the LTM8056 is flexible enough to accommodate a wide range of operating frequencies, a haphazardly chosen one may result in undesirable operation under certain operating or fault conditions. A frequency that is too high can reduce efficiency, generate excessive heat or even damage the device.
APPLICATIONS INFORMATION

LTM8056 if the output is overloaded or short circuited. A frequency that is too low can result in a final design that has too much output ripple or too large of an output capacitor or is even unstable.

Parallel Operation

Two or more LTM8056s may be combined to provide increased output current by configuring them as a master and a slave, as shown in Figure 1. Each LTM8056 is equipped with an I\text{OUTMON} and a CTL pin. The I\text{OUTMON} pin’s 0V to 1.2V signal reflects the current passing through the output sense resistor, while a voltage less than 1.2V applied to the CTL pin will limit the current passing through the output sense resistor. By applying the voltage of the master’s I\text{OUTMON} pin to the slave’s CTL pin, the two units will source the same current to the load, assuming each LTM8056 output current sense resistor is the same value.

1. Apply the FB resistor network to the master, choosing the proper values for the desired output voltage. Suggested values for popular output voltages are provided in Table 1.

2. Apply a FB resistor network to the individual slaves so that the resulting output is higher than the desired output voltage.

3. Apply the appropriate output current sense resistors between V\text{OUT} and I\text{OUT}. If the same value is used for the master and slave units, they will share current equally.

4. Connect the master I\text{OUTMON} to the slaves’ CTL pin through a unity gain buffer. The unity gain buffer is required to isolate the output impedance of the LTM8056 from the integrated pull-up on the CTL pins.

5. Tie the outputs together.

Note that this configuration does not require the inputs to be tied together, making it simple to power a single heavy load from multiple input sources. Ensure that each input power source has sufficient voltage and current sourcing capability to provide the necessary power. Please refer to the Maximum Output Current vs VIN and Input Current vs Output Current curves in the Typical Performance Characteristics section for guidance.

Paralleled LTM8056s should normally be allowed to switch in discontinuous mode enabled to prevent current from flowing from the output of one unit into another; that is, the MODE pin should be tied to LL. In some cases, operating the master in forced continuous (MODE open) and the slaves in discontinuous mode (MODE = LL) is desirable. If so, current from the output can flow into the master’s input. Please refer to Input Precaution in this section for a discussion of this behavior.

Minimum Input Voltage and RUN

The LTM8056 needs a minimum of 5V for proper operation, but system parameters may dictate that the device operate only above some higher input voltage. For example, a LTM8056 may be used to produce 12V\text{OUT}, but the input power source may not be budgeted to provide enough current if the input supply voltage is below 8V.

The RUN pin has a typical falling voltage threshold of 1.2V and a typical hysteresis of 25mV. In addition, the pin sinks 3µA below the RUN threshold. Based upon the...
above information and the circuit shown in Figure 2, the $V_{IN}$ rising (turn-on) threshold is:

$$V_{IN} = (3\mu A \times R1) + 1.225V \frac{R1+R2}{R2}$$

and the $V_{IN}$ falling turn-off threshold is:

$$V_{IN} = 1.2 \frac{R1+R2}{R2}$$

![Figure 2. This Simple Resistor Network Sets the Minimum Operating Input Voltage Threshold with Hysteresis](image)

**Minimum Input Voltage and SV_{IN}**

The minimum input voltage of the LTM8056 is 5V, but this is only if $V_{IN}$ and SV_{IN} are tied to the same voltage source. If SV_{IN} is powered from a power source at or above 5VDC, $V_{IN}$ can be allowed to fall below 5V and the LTM8056 can still operate properly. Some examples of this are provided in the Typical Applications section.

**Soft-Start**

Soft-start reduces the input power sources’ surge currents by gradually increasing the controller’s current. As indicated in the Block Diagram, the LTM8056 has an internal soft-start RC network. Depending upon the load and operating conditions, the internal network may be sufficient for the application. To increase the soft-start time, simply add a capacitor from SS to GND.

**Output Current Limit (I_{OUT})**

The LTM8056 features an accurate average output current limit set by an external sense resistor placed between $V_{OUT}$ and $I_{OUT}$ as shown in Figure 3. $V_{OUT}$ and $I_{OUT}$ internally connect to a differential amplifier that limits the current when the voltage $V_{OUT}-I_{OUT}$ reaches 58mV. The current limit is:

$$I_{OUT(LIM)} = \frac{58mV}{R_{SENSE}}$$

where $R_{SENSE}$ is the value of the sense resistor in ohms.

Most applications should use an output sense resistor as shown in Figure 3, if practical. The internal buck-boost power stage is current limited, but is nonetheless capable of delivering large amounts of current in an overload condition, especially when the output voltage is much lower than the input and the power stage is operating as a buck converter.

![Figure 3. Set The LTM8056 Output Current Limit with an External Sense Resistor](image)

When the voltage across the output sense resistor falls to about 1/10th of full scale, the LL pin pulls low. If there is no output sense resistor, and $I_{OUT}$ is tied to $V_{OUT}$, LL will be active low. Applying an output sense resistor and tying the LL and MODE pins together can improve performance—see Switching Mode in this section.

In high step-down voltage regulator applications, the internal current limit can be quite high to allow proper operation. This can potentially damage the LTM8056 in overload or short-circuit conditions. Apply an output current sense resistor to set an appropriate current limit to protect the LTM8056 against these fault conditions.

**Output Current Limit Control (CTL)**

Use the CTL input to reduce the output current limit from the value set by the external sense resistor applied between $V_{OUT}$ and $I_{OUT}$. The typical control range is between 0V and 1.2V. The CTL pin does not directly affect the input
APPLICATIONS INFORMATION

Current Limit. If this function is not used, leave CTL open. Drive CTL to less than about 50mV to stop switching. The CTL pin has an internal pull-up resistor to 2V.

Input Current Limit (IIN)

Some applications require that the LTM8056 draw no more than some predetermined current from the power source. Current limited power sources and power sharing are two examples. The LTM8056 features an accurate input current limit set by an external sense resistor placed between IIN and VIN as shown in Figure 4. VIN and IIN internally connect to a differential amplifier that limits the current when the voltage IIN-VIN reaches 50mV. The current limit is:

\[ I_{\text{IN(LIM)}} = \frac{50\text{mV}}{R_{\text{SENSE}}} \]

where RSENSE is the value of the sense resistor in ohms. If input current limiting is not required, simply tie IIN to VIN.

![Figure 4. Set the LTM8056 Input Current Limit with an External Sense Resistor](image)

Input Current Monitor (IINMON)

The IINMON pin produces a voltage equal to approximately 20 times the voltage of IIN-VIN. Since the LTM8056 input current limit engages when IIN-VIN = 50mV, IINMON will be 1V at maximum input current.

Output Current Monitor (IOUTMON)

The IOUTMON pin produces a voltage proportional to the voltage of VOUT-IOUT. Since the LTM8056 output current limit engages when VOUT-IOUT = 58mV, IOUTMON will be 1.2V at maximum output current.

Synchronization

The LTM8056 switching frequency can be synchronized to an external clock using the SYNC pin. Driving SYNC with a 50% duty cycle waveform is a good choice, otherwise maintain the duty cycle between about 10% and 90%. When synchronizing, a valid resistor value (that is, a value that results in a free-running frequency of 100kHz to 800kHz) must be connected from RT to GND.

While an RT resistor is required for proper operation, the value of this resistor is independent of the frequency of the externally applied SYNC signal. Be aware, however, that the LTM8056 will switch at the frequency prescribed by the RT value if the SYNC signal terminates, so choose an appropriate resistor value.

CLKOUT

The CLKOUT signal reflects the internal switching clock of the LTM8056. It is phase shifted by approximately 180° with respect to the leading edge of the internal clock. If CLKOUT is connected to the SYNC input of another LTM8056, the two devices will switch about 180° out of phase.

Input Precaution

In applications where the output voltage is deliberately pulled up above the set regulation voltage or the FB pin is abruptly driven to a new voltage, the LTM8056 may attempt to regulate the voltage by removing energy from the load for a short period of time after the output is pulled up. Since the LTM8056 is a synchronous switching converter, it delivers this energy to the input. If there is nothing on the LTM8056 input to consume this energy, the input voltage may rise. If the input voltage rises without intervention, it may rise above the absolute maximum rating, damaging the part. Carefully examine the input voltage behavior to see if the application causes it to rise.

In many cases, the system load on the LTM8056 input bus will be sufficient to absorb the energy delivered by the Module regulator. The power required by other devices will consume more than enough to make up for what
APPLICATIONS INFORMATION

The LTM8056 delivers. In cases where the LTM8056 is the largest or only power converter, this may not be true and some means may need to be devised to prevent the LTM8056’s input from rising too high. Figure 5a shows a passive crowbar circuit that will dissipate energy during momentary input overvoltage conditions. The break-down voltage of the Zener diode is chosen in conjunction with the resistor R to set the circuit’s trip point. The trip point is typically set well above the maximum $V_{IN}$ voltage under normal operating conditions. This circuit does not have a precision threshold, and is subject to both part-to-part and temperature variations, so it is most suitable for applications where the maximum input voltage is much less than the 60$V_{IN}$ absolute maximum. As stated earlier, this type of circuit is best suited for momentary overvoltages.

Figure 5a is a crowbar circuit, which attempts to prevent the input voltage from rising above some level by dumping energy to GND through a power device. In some cases, it is possible to simply turn off the LTM8056 when the input voltage exceeds some threshold. An example of this circuit is shown in Figure 5b. When the power source on the output drives $V_{IN}$ above a predetermined threshold, the comparator pulls down on the RUN pin and stops switching in the LTM8056. When this happens, the input capacitance needs to absorb the energy stored within the LTM8056’s internal inductor, resulting in an additional voltage rise. This voltage rise depends upon the input capacitor size and how much current is flowing from the LTM8056 output to input.

Switching Mode

The MODE pin allows the user to select either discontinuous mode or forced continuous mode switching operation. In forced continuous mode, the LTM8056 will not skip cycles, even when the internal inductor current falls to zero or even reverses direction. This has the advantage of operating at the same fixed frequency for all load conditions, which can be useful when designing to EMI or output noise specifications. Forced continuous mode, however, uses more current at light loads, and allows current to flow from the load back into the input if the output is raised above the regulation point. This reverse current can raise the input voltage and be hazardous if the input is allowed to rise uncontrollably. Please refer to Input Precautions in this section for a discussion of this behavior.

Forced continuous operation may provide improved output regulation when the LTM8056 transitions from buck, buck-boost or boost operating modes, especially at lighter loads. In such a case, it can be desirable to operate in forced continuous mode except when the internal inductor current is about to reverse. If so, apply a current sense resistor between $V_{OUT}$ and $I_{OUT}$ and tie the LL and MODE pins together. The LL pin is low when the current through the output sense resistor is about one-tenth the full-scale maximum. When the output current falls to this level, the LL pin will pull the MODE pin down, putting the LTM8056 in discontinuous mode, preventing reverse current from flowing from the output to the input. In the case
APPLICATIONS INFORMATION

where MODE and LL are tied together, a small capacitor (~0.1µF) from these pins to GND may improve the light load transient response by delaying the transition from the discontinuous to forced continuous switching modes. MODE may be tied to GND for the purpose of blocking reverse current if no output current sense resistor is used.

FB Resistor Divider and Load Regulation

The LTM8056 regulates its FB pin to 1.2V, using a resistor divider to sense the output voltage. The location at which the output voltage is sensed affects the load regulation. If there is a current sense resistor between VOUT and IOUT, and the output is sensed at VOUT, the voltage at the load will drop by the value of the current sense resistor multiplied by the output current. If the output voltage can be sensed at IOUT, the load regulation may be improved.

PCB Layout

Most of the headaches associated with PCB layout have been alleviated or even eliminated by the high level of integration of the LTM8056. The LTM8056 is nevertheless a switching power supply, and care must be taken to minimize EMI and ensure proper operation. Even with the high level of integration, you may fail to achieve specified operation with a haphazard or poor layout. See Figure 6 for a suggested layout. Ensure that the grounding and heat sinking are acceptable.

A few rules to keep in mind are:

1. Place the RFB and RT resistors as close as possible to their respective pins.
2. Place the Cin capacitor as close as possible to the VIN and GND connection of the LTM8056.
3. Place the Cout capacitor as close as possible to the VOUT and GND connection of the LTM8056.
4. Minimize the trace resistance between the optional output current sense resistor, ROUT, and VOUT. Minimize the loop area of the IOUT trace and the trace from VOUT to ROUT.

5. Minimize the trace resistance between the optional input current sense resistor (RIN) and VIN. Minimize the loop area of the IIN trace and the trace from VIN to RIN.
6. Place the Cin and Cout capacitors such that their ground current flow directly adjacent or underneath the LTM8056.

7. Connect all of the GND connections to as large a copper pour or plane area as possible on the top layer. Avoid breaking the ground connection between the external components and the LTM8056.

8. Use vias to connect the GND copper area to the board’s internal ground planes. Liberally distribute these GND vias to provide both a good ground connection and thermal path to the internal planes of the printed circuit board. Pay attention to the location and density of the thermal vias in Figure 6. The LTM8056 can benefit from the heat sinking afforded by vias that connect to internal GND planes at these locations, due to their proximity to internal power handling components. The optimum number of thermal vias depends upon the printed circuit board design. For example, a board might use very small via holes. It should employ more thermal vias than a board that uses larger holes.

Hot-Plugging Safely

The small size, robustness and low impedance of ceramic capacitors make them an attractive option for the input bypass capacitor of the LTM8056. However, these capacitors can cause problems if the LTM8056 is plugged into a live supply (see Linear Technology Application Note 88 for a complete discussion). The low loss ceramic capacitor combined with stray inductance in series with the power source forms an underdamped tank circuit, and the voltage at the VIN pin of the LTM8056 can ring to more than twice the nominal input voltage, possibly exceeding the LTM8056’s rating and damaging the part. If the input supply
is poorly controlled or the LTM8056 is hot-plugged into an energized supply, the input network should be designed to prevent this overshoot. This can be accomplished by installing a small resistor in series with V\textsubscript{IN}, but the most popular method of controlling input voltage overshoot is to add an electrolytic bulk capacitor to the V\textsubscript{IN} net. This capacitor’s relatively high equivalent series resistance damps the circuit and eliminates the voltage overshoot. The extra capacitor improves low frequency ripple filtering and can slightly improve the efficiency of the circuit, though it is likely to be the largest component in the circuit.

**Thermal Considerations**

The LTM8056 output current may need to be derated if it is required to operate in a high ambient temperature or deliver a large amount of continuous power. The amount of current derating is dependent upon the input voltage, output power and ambient temperature. The temperature rise curves given in the Typical Performance Characteristics section can be used as a guide. These curves were generated by a LTM8056 mounted to a 58cm\(^2\) 4-layer FR4 printed circuit board. Boards of other sizes and layer count...
Applications Information

Applications information can exhibit different thermal behavior, so it is incumbent upon the user to verify proper operation over the intended system's line, load and environmental operating conditions.

The thermal resistance numbers listed in the Pin Configuration of the data sheet are based on modeling the µModule package mounted on a test board specified per JESD 51-9 (Test Boards for Area Array Surface Mount Package Thermal Measurements). The thermal coefficients provided on this page are based on JESD 51-12 (Guidelines for Reporting and Using Electronic Package Thermal Information).

For increased accuracy and fidelity to the actual application, many designers use FEA to predict thermal performance. To that end, the Pin Configuration of the data sheet typically gives four thermal coefficients:

- $\theta_{JA}$ – Thermal resistance from junction to ambient.
- $\theta_{JCbottom}$ – Thermal resistance from junction to the bottom of the product case.
- $\theta_{JCtop}$ – Thermal resistance from junction to top of the product case.
- $\theta_{JB}$ – Thermal resistance from junction to the printed circuit board.

While the meaning of each of these coefficients may seem to be intuitive, JEDEC has defined each to avoid confusion and inconsistency. These definitions are given in JESD 51-12, and are quoted or paraphrased below:

- $\theta_{JA}$ is the natural convection junction-to-ambient air thermal resistance measured in a one cubic foot sealed enclosure. This environment is sometimes referred to as “still air” although natural convection causes the air to move. This value is determined with the part mounted to a JESD 51-9 defined test board, which does not reflect an actual application or viable operating condition.

- $\theta_{JCbottom}$ is the thermal resistance between the junction and bottom of the package with all of the component power dissipation flowing through the bottom of the package. In the typical µModule converter, the bulk of the heat flows out the bottom of the package, but there is always heat flow out into the ambient environment. As a result, this thermal resistance value may be useful for comparing packages but the test conditions don’t generally match the user’s application.

- $\theta_{JCtop}$ is determined with nearly all of the component power dissipation flowing through the top of the package. As the electrical connections of the typical µModule converter are on the bottom of the package, it is rare for an application to operate such that most of the heat flows from the junction to the top of the part. As in the case of $\theta_{JCbottom}$, this value may be useful for comparing packages but the test conditions don’t generally match the user’s application.

- $\theta_{JB}$ is the junction-to-board thermal resistance where almost all of the heat flows through the bottom of the µModule converter and into the board, and is really the sum of the $\theta_{JCbottom}$ and the thermal resistance of the bottom of the part through the solder joints and through a portion of the board. The board temperature is measured a specified distance from the package, using a 2-sided, 2-layer board. This board is described in JESD 51-9.

Given these definitions, it should now be apparent that none of these thermal coefficients reflects an actual physical operating condition of a µModule converter. Thus, none of them can be individually used to accurately predict the thermal performance of the product. Likewise, it would be inappropriate to attempt to use any one coefficient to correlate to the junction temperature versus load graphs given in the product’s data sheet. The only appropriate way to use the coefficients is when running a detailed thermal analysis, such as FEA, which considers all of the thermal resistances simultaneously.
A graphical representation of these thermal resistances is given in Figure 7.

The blue resistances are contained within the µModule converter, and the green are outside.

The die temperature of the LTM8056 must be lower than the maximum rating of 125°C, so care should be taken in the layout of the circuit to ensure good heat sinking of the LTM8056. The bulk of the heat flow out of the LTM8056 is through the bottom of the µModule converter and the BGA pads into the printed circuit board. Consequently a poor printed circuit board design can cause excessive heating, resulting in impaired performance or reliability. Please refer to the PCB Layout section for printed circuit board design suggestions.

![Figure 7](image-url)
**TYPICAL APPLICATIONS**

18V\textsubscript{OUT} Fan Power from 3V\textsubscript{IN} to 58V\textsubscript{IN} with Analog Current Control and 2A Input Current Limiting

![Diagram showing typical applications of the LTM8056](image)

**Output Voltage vs Output Current**

- **24V\textsubscript{OUT} from 9V\textsubscript{IN} to 58V\textsubscript{IN} with 1.1A Accurate Current Limit**

![Diagram showing output voltage vs output current](image)

For more information [www.linear.com/LTM8056](http://www.linear.com/LTM8056)
18VOUT from 18VIN to 58VIN with 2.5A Accurate Current Limit and Output Current Monitor

Two LTM8056s Paralleled to Get More Output Current. The Two µModules Are Synchronized and Switching 180° Out Of Phase

Output Voltage vs Output Current

Output Current per Channel vs Total Output Current
Two LTM8056s Powered from Different Input Sources to Run a Single Load. Each LTM8056 Draws No More Than 1.1A from Its Respective Power Sources, and Are Synchronized 180° Out Of Phase with Each Other

Input Current per Channel vs Total Output Current

For more information www.linear.com/LTM8056
## LTM8056

### PACKAGE DESCRIPTION

Table 4. LTM8056 Pin Assignment (Arranged by Pin Number)

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### PACKAGE PHOTO

![Package Photo](image-url)
PACKAGE DESCRIPTION

Please refer to http://www.linear.com/designtools-packaging/ for the most recent package drawings.
TYPICAL APPLICATION

14.4V, 3A Lead-Acid Battery Charger Input Current Limited to 2A

Maximum Input and Output Current vs Input Voltage

DESIGN RESOURCES

<table>
<thead>
<tr>
<th>SUBJECT</th>
<th>DESCRIPTION</th>
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</table>
| µModule Design and Manufacturing Resources | Design:  
- Selector Guides  
- Demo Boards and Gerber Files  
- Free Simulation Tools  

Manufacturing:  
- Quick Start Guide/Demo Manual  
- PCB Design, Assembly and Manufacturing Guidelines  
- Package and Board Level Reliability |
| µModule Regulator Products Search | 1. Sort table of products by parameters and download the result as a spreadsheet.  
2. Search using the Quick Power Search parametric table. |
| TechClip Videos | Quick videos detailing how to bench test electrical and thermal performance of µModule products. |
| Digital Power System Management | Linear Technology’s family of digital power supply management ICs are highly integrated solutions that offer essential functions, including power supply monitoring, supervision, margining and sequencing, and feature EEPROM for storing user configurations and fault logging. |

RELATED PARTS

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<th>DESCRIPTION</th>
<th>COMMENTS</th>
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<td>Higher Power, Pin Compatible</td>
<td>8.5A, 5V ≤ V_IN ≤ 36V</td>
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<td>LTM4605</td>
<td>Higher Power Buck-Boost (Up to 60W)</td>
<td>External Inductor, Synchronous Switching Buck-Boost; Up to 36V_IN, 0.8V ≤ V_OUT ≤ 16V</td>
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<td>LTM4607</td>
<td>Higher Power Buck-Boost (Up to 60W)</td>
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<td>Higher Power Buck-Boost (Up to 60W)</td>
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<td>SEPIC and Inverting; 700mA, 6.25mm × 11.25mm × 4.92mm BGA</td>
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<td>Isolated, Lower Power</td>
<td>Flyback Topology, 550mA (5V_OUT, 24V_IN), UL60950, 2kWAC</td>
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