The Intel® Atom™ is a popular processor for netbook PCs, mobile internet devices (MIDs) and deeply embedded computers used in applications from industrial control, to video surveillance, to in-vehicle infotainment. With the addition of NexFET™ technology from Texas Instruments (TI), system designers now have the ability to further reduce power consumption and form factor in their Atom power designs.

The latest NexFET technology has further optimized TI’s processor solutions for Intel’s IMVP6+ Atom® and IMVP6.5 embedded platforms. These MOSFETs provide an additional 5% power conversion efficiency over previous generations of MOSFETs available from other suppliers.

Complete System Solutions
### NexFET™ Selection Table

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<th>VDS</th>
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### 30-V, N-Channel NexFET™ Selection Table — Coming 1Q 2010

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For more information visit: [www.ti.com/imvp6](http://www.ti.com/imvp6)
TPS51610 IMVP6+ Intel Atom Core EVM

- OSR™ Setting for Reduction of Output Caps
- CSD16406Q3 3mm x 3mm NexFET™
- 1" x 0.5" DC/DC Converter PCB Area
- V CORE Terminals for Load Connection
- V CORE Static and Load Transient Check
- CSD16301Q2 2mm x 2mm NexFET
- IMVP6+ VID Setting
- Sleep-State Setting
- 5-V Bias
- Switching-Frequency Setting
- Current-Limit Setting
- V IN (Power Input)

TPS516121 40-A IMVP6.5 CPU Core EVM

- OSR™ Setting (For Output Caps Reduction)
- Current-Limit Setting
- Small 40-A DC/DC Converter PCB Area
- CSD16322Q5 and CSD16321Q5 NexFET™ MOSFETs
- Load Transient Setting
- 5-V Bias
- Switching Frequency Setting
- V IN (Power Input)
- TPS51621 IMVP6.5 CPU Core Controller
- Sleep-State Setting
- IMVP6.5 VID Setting

Atom Embedded Computer Board Featuring the TPS51610

- TPS51610
## TI PWR Solutions for Intel IMVP Embedded Platforms

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<th>Menlow/eMenlow (Z5xx/Silverthorne)</th>
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<th>Amplifiers</th>
<th>Applications</th>
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<td>Data Converters</td>
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<td>power.ti.com</td>
<td>Security</td>
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<tr>
<td>Microcontrollers</td>
<td>microcontroller.ti.com</td>
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<td>RF/IF and ZigBee® Solutions</td>
<td><a href="http://www.ti.com/4pr">www.ti.com/4pr</a></td>
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