World’s first chipset with 4x4 MIMO, dynamic digital beamforming, and support for 4 spatial streams with unequal modulation

Overview

The QHS710 chipset combines Quantenna’s QT2018B Baseband Chip and QT2518B Radio Frequency (RF) Chip. The fully compliant 802.11n QHS710 chipset supports 4x4 MIMO (4Tx 4Rx) with implicit/explicit transmit beamforming and operates in the 5GHz frequency band. The chipset has a wide variety of interfaces including dual RGMII/MII, PCI-e and SPI flash, and its dual embedded ARC processors provide full wireless Media Access Control (MAC) and network processing support.

Support for 4 spatial streams with unequal modulation per stream enables up to 600 Mbps PHY data rates and best-in-class performance. Support for Low-Density Parity Check (LDPC) enables efficient error correction coding for superior coverage and improved range.

As the core chipset, the QHS710 device enables applications such as high-definition video content delivery to wireless HDTVs, wireless routers, wireless video bridges for Set-top Boxes (STB), HDTVs with integrated video decoders, residential gateways (cable, fiber, xDSL), media servers and Home Network Storage (NAS) appliances with integrated Access Points (APs).

Specifications

- Frequency Band: 4.900–5.850 GHz
- Channel Bandwidth: 20MHz, 40MHz
- Supported I/O interfaces: PCI-e, RGMII/MII (x2)
- Supported Peripheral interfaces: I2c, SPI
- Memory interface: DDR2 DRAM
- Antenna connectors: Four 50 ohm, 5GHz U.FL

QHS710 BLOCK DIAGRAM